

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

<b>APPLICANT:</b>	<b>Chan et al.</b>	<b>GROUP:</b>	<b>2611</b>
<b>SERIAL NO:</b>	09/528,678	<b>EXAMINER:.</b>	Curtis B. Odom
<b>FILED:</b>	03/20/2000	<b>Date of mailing of Notice of Allowance:</b>	<b>07/11/2007</b>
<b>FOR:</b>	BLOCK-ITERATIVE EQUALIZERS FOR DIGITAL COMMUNICATION SYSTEM		

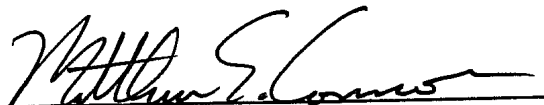
**Mail Stop Issue Fee**  
**Assistant Commissioner of Patents**  
**P.O. Box 1450, Alexandria, VA 22313-1450**  
**Attn: Official Draftsman**

**TRANSMITTAL OF FORMAL DRAWINGS**

Applicant submits herewith new drawing(s) for this application. Attached please find FOUR (4) sheets of formal drawings for this application.

The three month period of response set in the Notice of Allowability (PTOL 37) expires on October 11, 2007, and this submission is on or before this expiry date.

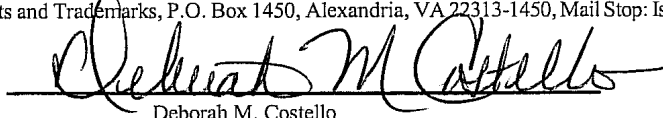
Respectfully submitted,



Matthew E. Connors  
Registration No. 33,298  
Gauthier & Connors LLP  
225 Franklin Street, Suite 2300  
Boston, Massachusetts 02110  
Telephone: (617) 426-9180  
Extension 112

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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited electronically with the United States Postal Service on the date shown below addressed to the Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, Mail Stop: Issue Fee.



Deborah M. Costello

Date: 8-24-07